

Electronic Acknowledgement Receipt

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Application Number:	10647247
International Application Number:	
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Title of Invention:	METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, AND METHOD OF FORMING RESIST PATTERN
First Named Inventor/Applicant Name:	Hajime Yamamoto
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1		031029_Amendment_1114_02_04_2010.pdf	311180 35369f7405a4b606cbf2fe6cdd7ec6499b87f68b	yes	11
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment Submitted/Entered with Filing of CPA/RCE		1	1	
	Claims		2	8	
	Applicant Arguments/Remarks Made in an Amendment		9	11	
Warnings:					
Information:					
2	Request for Continued Examination (RCE)	031029_RCE_Transmittal_02_04_2010.pdf	768825 4775d0a3fa6e14a7521cb22fdc35ad13aedac765	no	3
Warnings:					
Information:					
3	Fee Worksheet (PTO-875)	fee-info.pdf	30599 47dbbf061fd179c38230130bc87103e76a5b5b4c	no	2
Warnings:					
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Total Files Size (in bytes):			1110604		
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